

# Process algebra

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## Abstract

Process algebra is the study of distributed or parallel systems by algebraic means. Originating in computer science, process algebra has been extended in recent years to encompass not just discrete-event systems, but also continuously evolving phenomena, resulting in so-called hybrid process algebras. A hybrid process algebra can be used for the specification, simulation, control and verification of embedded systems in combination with their environment, and for any dynamic system in general. As the vehicle of our exposition, we use the hybrid process algebra  $\chi$  (Chi). The syntax and semantics of  $\chi$  are discussed, and it is explained how equational reasoning simplifies tool implementations for simulation and verification. A bottle filling line example is introduced to illustrate system analysis by means of equational reasoning.

## 1 Introduction

### 1.1 Definition

Process algebra is the study of distributed or parallel systems by algebraic means. The word ‘process’ here refers to *behavior* of a *system*. A system is anything showing behavior, such as the execution of a software system, the actions of a machine or even the actions of a human being. Behavior is the total of events, actions or evolutions that a system can perform, the order in which these can be executed and maybe other aspects of this execution such as timing, probabilities, or continuous aspects. Always, the focus is on certain aspects of behavior, disregarding other aspects, so an abstraction or idealization of the ‘real’ behavior is considered. Instead of considering behavior, we may consider an *observation* of behavior, where an action is the chosen unit of observation. As the origin of process algebra is in computer science, the actions are usually thought to be discrete: occurrence is at some moment in time, and different actions are separated in time. This is why a process is sometimes also called a *discrete-event system*. Please note that this is a less restrictive definition than e.g. (Cassandras and Lafortune 1999).

The word ‘algebra’ denotes that the approach in dealing with behavior is algebraic and axiomatic. That is, methods and techniques of universal algebra are used. A process algebra can be defined as any mathematical structure satisfying the axioms given for the basic operators. A process is an element of a process algebra. By using the axioms, we can perform *calculations* with processes. Often, though, process algebra goes beyond the strict bounds of universal algebra: sometimes multiple sorts and/or binding of variables are used.

The simplest model of behavior is to see behavior as an input/output function. A value or input is given at the beginning of the process, and at some moment there is a value as outcome or output.

This model was used to advantage as the simplest model of the behavior of a computer program in computer science, from the start of the subject in the middle of the twentieth century. It was instrumental in the development of (finite state) *automata theory*. In automata theory, a process is modeled as an automaton. An automaton has a number of *states* and a number of *transitions*, going from a state to a state. A transition denotes the execution of an (elementary) action, the basic unit of behavior. Also, there is an initial state (sometimes, more than one) and a number of final states. A behavior is a run, i.e. a path from initial state to final state. An important aspect is when to consider two automata equal, expressed by a notion of equivalence. On automata, the basic notion of equivalence is ‘language equivalence’, which considers equivalence in terms of behavior, where a behavior is characterized by the set of executions from the initial state to a final state. An algebra that allows equational reasoning about automata is the algebra of regular expressions, see e.g. (Linz 2001).

Later on, this model was found to be lacking in several situations. Basically, what is missing is the notion of *interaction*: during the execution from initial state to final state, a system may interact with another system. This is needed in order to describe parallel or distributed systems, or so-called *reactive* systems. When dealing with interacting systems, the phrase *concurrency theory* is used. Thus, concurrency theory is the theory of interacting, parallel and/or distributed systems. When referring to process algebra, we usually consider it as an approach to concurrency theory, so that a process algebra usually (but not necessarily) has an operator (function symbol) to put things in parallel, called parallel composition.

Thus, a usable definition is that process algebra is the study of the behavior of parallel or distributed systems by algebraic means. It offers means to describe or *specify* such systems, and thus it has means to specify parallel composition. Besides this, it can usually also specify alternative composition (put things in a choice) and sequential composition (sequencing, put things one after the other). Moreover, it is possible to reason about such systems using algebra, i.e. equational reasoning. By means of this equational reasoning, *verification* becomes possible, i.e. it can be established that a system satisfies a certain property.

What are these basic laws of process algebra? In this chapter, we do not present collections of such laws explicitly. Rather, it is shown how calculations can proceed.

To repeat, it can be said that any mathematical structure with operators of the right number of arguments satisfying the given basic laws is a process algebra. Often, these structures are formulated in terms of *transition systems*, where a transition system has a number of states (including an initial state and a number of final states) and transitions between them. The notion of equivalence studied is usually not language equivalence. Prominent among the equivalences studied is the notion of *bisimulation*. Often, the study of transition systems, ways to define them and equivalences on them are also considered part of process algebra, even in the case no equational theory is present.

## 1.2 Calculation

One form of calculation is verification by means of automated methods (called *model checking*, see e.g. (Clarke, Grumberg, and Peled 2000)) that traverse all states of a transition system and check that a certain property is true in each state. The drawback is that transition systems grow at a rate exponential in the number of components (in fact, due to the presence of parameters, often they become infinite). For instance, a system having 10 interacting components, each of which has 10 states, has a total number of 10 000 000 000 states. It is said that model checking techniques suffer from the *state explosion* problem.

At the other end, reasoning can take place in logic, using a form of deduction. Also here,

progress is made, and many *theorem proving* tools exist (Bundy 1999). The drawback here is that finding a proof needs user assistance (as the general problem is undecidable), which requires a lot of knowledge about the system.

Equational reasoning on the basis of an algebraic theory takes the middle ground. On the one hand, the next step in the procedure is usually clear, since it is more rewriting than equational reasoning. Therefore, automation can be done in a straightforward way. On the other hand, representations are compact and allow the presence of parameters, so that an infinite set of instances can be verified at the same time.

### 1.3 History

Process algebra started in the late seventies of the twentieth century. At that point, the only part of concurrency theory that existed was the theory of Petri nets, as discussed in another chapter in this volume.

The question was raised how to give semantics to programs containing a parallel composition operator. It was found that this was difficult using the semantic methods used at that time. The idea of a behavior as an input/output function needed to be abandoned. A program could still be modeled as an automaton, but the notion of language equivalence was no longer appropriate. This is because the interaction a process has between input and output influences the outcome, disrupting functional behavior. Secondly, the notion of *global* variables needed to be overcome. Using global variables, a state of an automaton used as a model was given as a valuation of the program variables, that is, a state was determined by the values of the variables. The independent execution of parallel processes makes it difficult or impossible to determine the values of global variables at a given moment. It turned out to be simpler to let each process have its own local variables, and to denote exchange of information explicitly.

After some preliminary work by others, three main process algebra theories were developed. These are CCS (Calculus of Communicating Systems) by Robin Milner (Milner 1980; Milner 1989), CSP (Communicating Sequential Processes) by Tony Hoare (Hoare 1985), and ACP (Algebra of Communicating Processes) by Jan Bergstra and Jan Willem Klop, see (Bergstra and Klop 1984; Baeten and Weijland 1990).

Comparing these best-known process algebras CCS, CSP and ACP, we can say there is a considerable amount of work and applications realized in all three of them. In that sense, there seem to be no fundamental differences between the theories with respect to the range of applications. Historically, CCS was the first with a complete theory. Different from the other two, CSP makes fewer distinctions between processes. More than the other two, ACP emphasizes the algebraic aspect: there is an equational theory with a range of semantic models. Also, ACP has a more general communication scheme: in CCS, communication is combined with abstraction, in CSP, there is also a restricted communication scheme.

Over the years, other process algebras were developed, and many extensions were realized. Most interesting for this volume is the extension to hybrid systems. The language we consider in this chapter is most closely related to the ACP approach, as in this approach, there is the most work and experience on hybrid extensions. For a taste of another approach, see (He 1994).

### 1.4 Hybrid process algebra

Process algebra started out in computer science, and is especially geared to describing discrete-event systems such as computer programs and software systems. With the growing importance of embedded systems, which are software systems that are integrated in the machine or device

that they control, it was considered to use process algebra also to model and reason about the controlled physical environment of the software. However, specifications of physical systems not only require discrete-event models (such as timed or untimed transition systems), but also continuous-time models (such as differential algebraic equations (Kunkel and Mehrmann 2006)), leading to hybrid models.

In recent years, several attempts were made to incorporate such aspects into process algebra. In this chapter, we report on one of these, based on the  $\chi$  language. Other hybrid process algebras are HyPA (Cuijpers and Reniers 2005), process algebra for hybrid systems  $ACP_{hs}^{st}$  (Bergstra and Middelburg 2005), and the  $\phi$ -Calculus (Rounds and Song 2003). The history of the  $\chi$  formalism dates back quite some time. It was originally mainly used as a modeling and simulation language for discrete-event systems. The first simulator (Naumoski and Alberts 1998) was successfully applied to a large number of industrial cases, such as integrated circuit manufacturing plants, breweries, and process industry plants (Beek, van den Ham, and Rooda 2002). Later, the hybrid language and simulator were developed (Fábián 1999; Beek and Rooda 2000). Recently, the  $\chi$  language has been completely redesigned. The result is a hybrid process algebra with a formal semantics as defined in (Beek, Man, Reniers, Rooda, and Schiffelers 2006). This chapter informally defines the most important elements of the syntax and semantics of the  $\chi$  process algebra. It also extends the formal definitions of (Beek, Man, Reniers, Rooda, and Schiffelers 2006) with a more user friendly syntax, including the specification of data types.

## 2 Syntax and informal semantics of the $\chi$ process algebra

In this section the syntax and informal semantics of the  $\chi$  process algebra is first illustrated by means of two examples: a controlled tank and an assembly line example. After this intuitive explanation, the syntax and semantics are more precisely defined.

### 2.1 Controlled tank

Figure 1 shows a liquid storage tank with a volume controller  $VC$ . The incoming flow  $Q_i$  is controlled by means of a valve  $n$ . The outgoing flow is given by equation  $Q_o = \sqrt{V}$ . The volume controller maintains the volume  $V$  of the liquid in the tank between 2 and 10. The  $\chi$  model of the controlled tank is as follows:

```

model Tank() =
  || var n : nat = 0, cont V : real = 10, alg Qi, Qo : real
  ::  $\dot{V} = Q_i - Q_o$ 
  ||  $Q_i = n \cdot 5$ 
  ||  $Q_o = \sqrt{V}$ 
  || *(  $V \leq 2 \rightarrow n := 1$ ;  $V \geq 10 \rightarrow n := 0$  )
  ||

```

Figure 2 shows the result of a simulation of the model for 7 time units. Initially, the volume in the tank equals 10, and the valve is closed ( $n = 0$ ). The derivative of the volume equals the difference between the incoming and outgoing flows ( $\dot{V} = Q_i - Q_o$ ). The specification of the controller is given by  $*( V \leq 2 \rightarrow n := 1; V \geq 10 \rightarrow n := 0 )$ , where the loop statement  $*(p)$  denotes the infinite repetition of statement  $p$ . The guard operator ‘ $\rightarrow$ ’ is used to specify conditional execution of a statement, by prefixing a condition (referred to as a guard)  $b$  to a statement  $p$ , which is written as  $b \rightarrow p$ . The sequential composition operator ‘;’ is used to specify sequential execution of

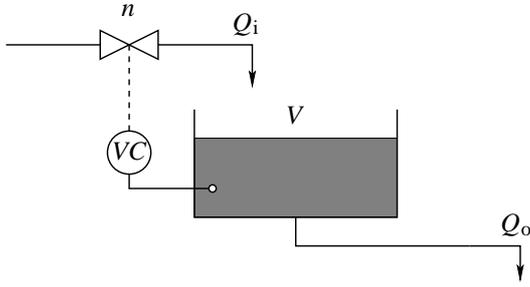


Figure 1: Controlled tank.

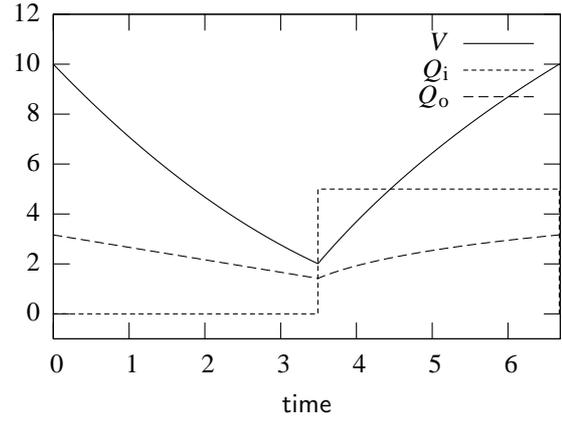


Figure 2: Simulation of the controlled tank.

components, and the parallel composition operator ‘||’ is used to specify the parallel execution of components. In the example, the equations and the controller are all executed in parallel.

Initially, the three equations are enabled and the guard  $V \leq 2$  is enabled. Since the value of the guard is false initially ( $V$  equals 10), the assignment  $n := 1$  is disabled. The model executes by doing a sequence of delays, which involve passing of time, and actions, which are executed instantaneously, without passing of time. The model can do a delay of  $t$  time units when all enabled statements can simultaneously do a delay of  $t$ . A guard that is false, allows arbitrary delays until it becomes true (see Section 2.6.2), and equations allow a delay of  $t$ , when a solution of the equations exists that defines the values of the variables as a function of time (on domain  $[0, t]$ ). At the end point of the delay(s),  $V$  equals 2, and the guard becomes true. The assignment  $n := 1$  is now enabled. The model can now no longer delay, since assignments cannot delay; an assignment is a so called ‘non-delayable’ statement (see Section 2.3). The model can do an action when any of the enabled statements can do an action. Assignments can do an action by executing the assignment. Therefore, the model executes the assignment  $n := 1$ , which models opening of the valve. The assignment causes the value of variable  $Q_i$  to immediately become 5, to satisfy the equation  $Q_i = n \cdot 5$ . This is referred to as the ‘consistent equation semantics’: equations must be satisfied at all times. The value of the continuous variable  $V$ , however, is unchanged; only algebraic variables are allowed to change, to satisfy equations, when other variables are assigned. Execution of the assignment  $n := 1$  causes the assignment to be disabled, and the next statement ( $V \geq 10 \rightarrow n := 0$ ) to be enabled. The guard  $V \geq 10$  is false. Therefore, the model delays, while solving the equations, until the guard becomes true (volume in the tank equals 10). Now the assignment  $n := 0$  is executed, modeling closing of the valve. As a result, the assignment is disabled, and the first statement ( $V \leq 2 \rightarrow n := 1$ ) of the repetition is re-enabled.

The general form of a  $\chi$  model is:

$$\text{model } id(D_m) = \llbracket D :: p \rrbracket,$$

where  $id$  is an identifier that represents the name of the model,  $D_m$  denotes the model parameters, that are not present in the example; and  $D$  denotes the declaration of variables and/or channels of the model. Channels are introduced in the assembly line example of Section 2.2. Finally,  $p$  denotes a statement, also known as a process term. Notation  $\llbracket D :: p \rrbracket$  is in fact a scope operator, which is defined in Section 2.3, together with statement  $p$ . The following kinds of variable can be declared in  $D$ :

- ‘Discrete’ variables, such as in `var n : nat = 0`. This declares a variable  $n$  with initial value 0. The name discrete is common in hybrid systems terminology, and refers to the fact that the variable takes only a limited number of values when the model is executed (in this case only 0 and 1). The value of a discrete variable remains constant when model time progresses. The value, in principle, changes only by means of assignments (e.g.  $n := 1$ ). Discrete variables can be of type real, however.
- ‘Continuous’ variables, such as in `cont V : real = 10`. Continuous variables are the only variables for which dotted variables (derivatives) can be used in models. Therefore, the declaration `cont V : real` implies that  $V$  and its dotted version  $\dot{V}$ , can both be used in the model. The values of continuous variables may change according to a continuous function of time when model time progresses. The values of continuous variables are further restricted by equations (or in more general terms: delay predicates, defined in Section 2.5.2). The value of a continuous variable can also be changed by means of an assignment.
- ‘Algebraic’ variables, such as in `alg Qi, Qo : real`. These variables behave in a similar way as continuous variables. The differences are that algebraic variables may change according to a discontinuous function of time, algebraic variables are not allowed to occur as dotted variables, and algebraic variables do not have a memory: the value of an algebraic variable is in principle determined by the enabled equations, and not by assignments (e.g.  $Q_o = \sqrt{V}$ ).

Finally, a predefined reserved global variable `time`, which denotes the model time, exists. The value of this variable is initially zero, and it is incremented by  $t$  whenever the model does a delay of  $t$ .

## 2.2 Assembly line example

An assembly process  $A$  assembles three different parts that are supplied by three suppliers  $G$ . The order in which the parts are supplied is unknown, but each part should be received by the assembly process as soon as possible. When all three parts have been received, assembly may start. Assembly takes  $t_A$  units of time. When the products have been assembled, they are sent to an exit process  $E$ . Figure 3 shows the iconic model of the assembly line, which is modeled as a discrete-event system. For the  $\chi$  model of the assembly line, first two types are declared. The type

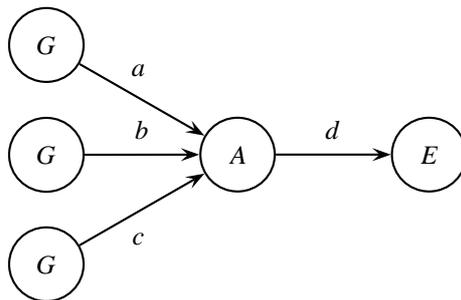


Figure 3: Iconic model of an assembly line.

‘part’, representing a part as a natural number, and the type ‘assy’, representing an assembled unit as a 3-tuple of parts:

```

type part = nat
  , assy = (part, part, part)

```

The  $\chi$  model consists of parallel instantiations of the three generator processes  $G$ , the assembly process  $A$  and the exit process  $E$ :

```

model AssemblyLine(val  $t_0, t_1, t_2, t_A$  : real) =
  || chan  $a, b, c$  : part,  $d$  : assy
  ::  $G(a, 0, t_0) \parallel G(b, 1, t_1) \parallel G(c, 2, t_2) \parallel A(a, b, c, d, t_A) \parallel E(d)$ 
  ||

```

The channels  $a, b, c, d$  are used for communication and synchronization between the parallel processes. Each generator  $G$  sends a part  $n$  every  $t$  time units:

```

proc  $G(\text{chan } a! : \text{part}, \text{val } n : \text{nat}, t : \text{real}) = \ll *(\Delta t; a!n) \gg$ 
```

The assembly process receives the parts by means of the parallel composition ( $a?x \parallel b?y \parallel c?z$ ). This ensures that each part is received as soon as possible. The parallel composition terminates when all parts have been received.

```

proc  $A(\text{chan } a?, b?, c? : \text{part}, d! : \text{assy}, \text{val } t : \text{real}) =
  || \text{var } x, y, z : \text{part}
  :: *( ( a?x \parallel b?y \parallel c?z ) ; \Delta t; d!(x, y, z) )
  ||$ 
```

The exit process is simply:

```

proc  $E(\text{chan } a? : \text{assy}) = \ll \text{var } x : \text{assy} :: *(a?x) \gg$ 
```

To understand the meaning of the model, the process instantiations can be replaced by their definitions, as defined in (Beek, Man, Reniers, Rooda, and Schiffelers 2006), and the model parameters can be replaced by their values. Thus, the model instantiation  $\text{AssemblyLine}(5, 6, 7, 2)$  can be rewritten into the following equivalent form:

```

model AssemblyLine() =
  || chan  $a, b, c$  : part,  $d$  : assy
  :: || \text{var } n : \text{nat} = 0, t : \text{real} = 5 :: *(\Delta t; a!n) \gg
  || || \text{var } n : \text{nat} = 1, t : \text{real} = 6 :: *(\Delta t; b!n) \gg
  || || \text{var } n : \text{nat} = 2, t : \text{real} = 7 :: *(\Delta t; c!n) \gg
  || || \text{var } x, y, z : \text{part}, t : \text{real} = 2 :: *( ( a?x \parallel b?y \parallel c?z ) ; \Delta t; d!(x, y, z) ) \gg
  || || \text{var } x : \text{assy} :: *(d?x) \gg
  ||

```

Initially, the first statements of the repetitions are enabled. The first statement of the repetition of the assembly process is a parallel composition of three receive statements ( $a?x \parallel b?y \parallel c?z$ ). Enabling a parallel composition enables its components. Therefore, initially the statements  $\Delta t$ ,  $\Delta t$ ,  $\Delta t$ ,  $a?x$ ,  $b?y$ ,  $c?z$ , and  $d?x$  are enabled. Each of these statements can delay. A delay statement  $\Delta t$  behaves as a timer that can delay for at most  $t$  time units. After this, the timer is expired and can terminate by means of an action. The values of the three local variables  $t$  are 5, 6, and 7, respectively. Therefore, initially, a (maximum) delay of 5 time units is possible. After this, the first timer terminates by means of an action, and the send statement  $a!n$  is enabled. The enabled statements are now  $a!n$ ,  $\Delta t$ ,  $\Delta t$ ,  $a?x$ ,  $b?y$ ,  $c?z$ ,  $d?x$ , where the two timers modeled by  $\Delta t$  and  $\Delta t$  can delay for 1 and 2 remaining time units, respectively, before expiring. We now have an enabled pair of a send and a receive statement on the same channel that are placed in

parallel:  $a ! n$  and  $a ? x$ . This pair can simultaneously do a send and a receive action, followed by joint termination. The result is comparable to the (distributed) execution of the assignment  $x := n$ , or  $x := 0$ , since the value of the first variable  $n$  is 0. After this, the send and receive statements are disabled. Disabling of  $a ! n$  enables the delay statement  $\Delta t$  again. The enabled statements are now:  $\Delta t, \Delta t, \Delta t, b ? y, c ? z, d ? x$ , where the three timers need to delay for another 5, 1, and 2 time units, respectively, before expiring. After expiration of the second and third timer, communication of 1 via channel  $b$  and 2 via channel  $c$  takes place, respectively. Then, the parallel composition terminates, enabling the delay statement  $\Delta t$  of the assembly process. After this intuitive explanation of the  $\chi$  language by means of examples, the next sections more precisely define the syntax and semantics.

### 2.3 Statement syntax

This section defines the syntax of a considerable and representative subset of  $\chi$  models using a Backus-Naur (BNF) like notation. The symbol  $|$  defines choice, and notation  $\{Z\}^*$  denotes a sequence of zero or more  $Z$ 's. Statements can be divided in two classes: the atomic statements, that represent the smallest statement units; and the compound statements, that are constructed from one or more (atomic) statements by means of operators. The syntax of the atomic  $\chi$  statements, is as follows:

$p_{\text{atom}} ::=$	<code>skip</code>	non-delayable action
	<code>  <math>\mathbf{x} := \mathbf{e}</math></code>	non-delayable (multi-)assignment
	<code>  [skip]</code>	delayable action
	<code>  [<math>\mathbf{x} := \mathbf{e}</math>]</code>	delayable (multi-)assignment
	<code>  <math>h ! \mathbf{e}</math>   <math>h !</math></code>	delayable send
	<code>  <math>h ? \mathbf{x}</math>   <math>h ?</math></code>	delayable receive
	<code>  <math>\Delta d</math></code>	delay
	<code>  <math>u</math></code>	delay predicate,

where  $\mathbf{x}$  and  $\mathbf{e}$  denote comma separated variables  $x_1, \dots, x_n$  and expressions  $e_1, \dots, e_n$ , respectively, for  $n \geq 1$ ,  $h$  denotes a channel, and  $d$  denotes an expression of type real. Delay predicate  $u$  denotes a predicate over variables (including the variable time) and dotted continuous variables (derivatives). Delay predicates may occur in the form of differential algebraic equations, such as  $\dot{x} = y$ ,  $y = n$ , or in the form of a constraint or invariant, such as  $x \geq 1$ .

The syntax of the compound  $\chi$  statements is as follows:

$p ::=$	$p_{\text{atom}}$	atomic
	<code>  <math>p ; p</math></code>	sequential composition
	<code>  <math>b \rightarrow p</math></code>	guard operator
	<code>  <math>p \square p</math></code>	alternative composition
	<code>  <math>p \parallel p</math></code>	parallel composition
	<code>  <math>*p</math></code>	loop statement
	<code>  <math>b \xrightarrow{*} p</math></code>	while statement
	<code>  <math>\llbracket D :: p \rrbracket</math></code>	variable and channel scope operator
	<code>  <math>id(\mathbf{e})</math></code>	process instantiation
	<code>  <math>p_{\mathbb{R}}</math></code>	recursion scope operator (see Sections 3.2 and 3.3),

where guard  $b$  denotes a predicate over variables. The operators are listed in descending order of their binding strength as follows:  $\{*, \xrightarrow{*}, \rightarrow\}, ;, \{\square, \parallel\}$ . The operators inside the braces have

equal binding strength. Parentheses may be used to group statements. For example,  $x := 1; y := x \parallel x := 2; y := 2x$  means  $(x := 1; y := x) \parallel (x := 2; y := 2x)$ . To avoid confusion, parentheses are obligatory when alternative composition and parallel composition are used together. E.g.  $p \parallel q \parallel r$  is not allowed and should either be written as  $(p \parallel q) \parallel r$ , or as  $p \parallel (q \parallel r)$ .

## 2.4 Semantic framework

In this chapter, the meaning (semantics) of a  $\chi$  model is informally defined in terms of delay behavior and action behavior, based on the formal semantics as presented in (Beek, Man, Reniers, Rooda, and Schiffelers 2006). Delay behavior involves passing of time, where the semantics defines for each variable how its value changes as a function of time. Action behavior is instantaneous: time does not progress, and the semantics defines for each variable the relation between its value before and after the action.

Atomic statements can be disabled or enabled. Actions and delays are done by *enabled atomic* statements, with one exception only: an enabled guarded statement  $b \rightarrow p$ , with a guard that is false can do any delay. Atomic statements terminate by doing an action. They never terminate by doing a delay. A statement that terminates becomes disabled by doing so.

Compound statements combine (sub-)statements by means of operators. The operator defines the relation between enabling, disabling and termination of the compound statement and its sub-statements. Enabling or disabling a compound statement is defined in terms of enabling or disabling its sub-statements. Enabling a compound statement implies enabling one or more of its sub-statements. E.g. enabling a sequential composition  $p_1; \dots; p_n$  implies enabling the first statement  $p_1$ , whereas enabling a parallel composition  $p_1 \parallel \dots \parallel p_n$  implies enabling all statements  $p_1 \dots p_n$ .

Execution of a  $\chi$  model  $M$ , defined as model  $M(D_0) = \llbracket D_1 :: p_0 \rrbracket$ , takes place by executing a sequence of delays and actions in the following way:

- At the start, statement  $p_0$  is enabled.
- Any enabled skip statement or assignment statement (delayable or non-delayable) can do an action.
- An enabled pair of a send and a receive statement on the same channel that are placed in parallel can simultaneously do a send and a receive action, followed by joint termination. The result, in terms of values of variables, is comparable to the (distributed) execution of a (multi)-assignment. E.g. execution of the communication action in  $h ! 1 \parallel h ? x$  is comparable to execution of the assignment  $x := 1$ .
- The model can do delays only when and for as long as:
  - All enabled statements can delay. The delayable versions of the skip statement, assignment, and send and receive statements can always delay (the non-delayable versions can never delay). A delay statement  $\Delta d$  can delay for as long as its internal timer is not expired (see Section 2.5.3), and the set of all enabled delay predicates can delay for as long as they have a solution. Such a solution defines the values of the variables as a function of time for the period of the delay.

Note that the set of enabled statements may change while delaying. The reason for this is the guarded statement  $b \rightarrow p$ , because the value of the guard can change while delaying, due to changes in the values of continuous or algebraic variables used in  $b$ .

- No parallel pair of a send and a receive statement on the same channel is enabled or becomes enabled. This is because, by default, channels in  $\chi$  are urgent: communication or synchronization cannot be postponed by delaying.
- When different actions and/or delays are possible, any of these can be chosen. This is referred to as nondeterministic choice. Note that delays may always be shorter than the maximum possible length.

The values of the discrete and continuous variables are stored in memory. The values of the algebraic variables are not stored. This means that the starting point of the trajectory of a discrete or continuous variable equals its last value stored in memory. The starting point of the trajectory of an algebraic variables can be any value that is allowed by the enabled equations.

In models of physical systems, the delay behavior of the continuous and algebraic variables is usually uniquely determined: there is usually only one solution of the set of enabled differential algebraic equations. Multiple delays / solutions can be caused by under-specified systems of equations, where there are less equations than variables, or by delay predicates that allow multiple solutions, such as ‘true’ or  $\dot{x} \in [0, 1]$ .

The action behavior of the discrete, continuous and algebraic variables is as follows:

- The discrete and continuous variables do not change as a result of actions unless the change is explicitly specified, for example by means of an assignment, or by receiving a value via a channel.
- The algebraic variables can, in principle, change arbitrarily in actions. In most models, their values are defined by equations.

## 2.5 Semantics of atomic statements

### 2.5.1 Skip and multi-assignment

An enabled skip statement can do an action, and then terminates. It corresponds to an assignment  $x := x$ , because the values of continuous and discrete variables are left unchanged. The skip statement can be used to make a choice in an alternative composition statement, because it executes an action (see process *Tank* in Section 3.2).

An enabled *multi-assignment* statement  $\mathbf{x}_n := \mathbf{e}_n$  for  $n \geq 1$  can do an action that changes the values of the variables  $x_1, \dots, x_n$  in one step to the values of expressions  $e_1, \dots, e_n$ , respectively, and then terminates. For  $n = 1$ , this gives a normal assignment  $x := e$ .

### 2.5.2 Delay predicate

An enabled *delay predicate*  $u$  can perform delays but no actions. Delay predicates restrict the allowed trajectories of the variables while delaying in such a way that at each time point during the delay the delay predicate holds (its value must be true), when all variables and dotted variables in the predicate are replaced by their current value.

Delay predicates also restrict the action behavior of  $\chi$  models, because the enabled delay predicates must also hold before and after each action. In fact, the enabled delay predicates of a  $\chi$  model must hold at all times. This is referred to as the ‘consistent equation semantics’.

The relation between the trajectory of a continuous variable  $x$  and the trajectory of its ‘derivative’  $\dot{x}$  is given by the Caratheodory solution concept:  $x(t) = x(0) + \int_0^t \dot{x}(s)ds$ . This

allows a non-smooth (but continuous) trajectory for a differential variable  $x$  in the case that the trajectory of its ‘derivative’  $\dot{x}$  is non-smooth or even discontinuous, as in, for example, model  $M() = \llbracket \text{cont } x : \text{real} = 0 :: \dot{x} = \text{step}(\text{time} - 1) \rrbracket$ , where  $\text{step}(y)$  equals 0 for  $y \leq 0$  and 1 for  $y > 0$ .

### 2.5.3 Delay statement

A delay statement  $\Delta d$  behaves as a timer that can be in three modes: reset, running or expired. A timer that is in mode running keeps track of the remaining time  $t_{\text{exp}}$  before expiring. Initially, timers are in mode reset. In modes reset and running, a timer can delay; in mode expired, it can terminate by means of an action. If the timer is enabled, its behavior is as follows:

- In mode reset, when the value  $c$  of expression  $d$  is bigger than zero, the timer can do a delay  $t$  for  $t \leq c$ . If  $t < c$ , the new mode after the delay is running with  $t_{\text{exp}} = c - t$ . If  $t = c$ , the new mode is expired.
- In mode running, the timer can do a delay  $t \leq t_{\text{exp}}$  to mode running ( $t < t_{\text{exp}}$ ) or expired ( $t = t_{\text{exp}}$ ). It switches to mode reset when it is disabled as a result of a choice being made in an alternative composition (see Section 2.6.3).

E.g. in  $x := 0; *(\Delta 3 \parallel \dot{x} = 1 \parallel x \geq 1 \rightarrow x := 0)$ , when the delay statement / timer  $\Delta 3$  becomes running, it switches to mode reset after 1 time unit, because of execution of the (second, guarded) assignment  $x := 0$ , which enforces a choice in the alternative composition and disables the timer.

- In mode expired, or in mode reset when the value  $c$  of expression  $d$  equals zero, the timer can do an action, accompanied by termination to mode reset. It also switches to mode reset when it is disabled as a result of a choice being made in an alternative composition.

The mode of a timer remains unchanged when it is disabled as a result of the value of a guard becoming false. E.g. in  $\sin(2\pi \text{time}) \geq 0 \rightarrow \Delta 1$ , the timer expires after two time units, that is after two periods of the sine function, because the timer only delays when the sine function is positive. As a final example, consider  $*(h ? d; \Delta d) \parallel *(h ! 1; h ! 2)$ . The first delay of the timer is 1, the second delay is 2, and then the cycle is repeated.

## 2.6 Semantics of compound statements

### 2.6.1 Sequential composition

In a *sequential composition*  $p_1; \dots; p_n$  ( $n \geq 1$ ), only one statement  $p_i$ ,  $1 \leq i \leq n$ , can be enabled at the same time. Enabling a sequential composition  $p_1; \dots; p_n$  implies enabling its first statement  $p_1$ . When statement  $p_i$  ( $1 \leq i \leq n - 1$ ) terminates (and is therefore also disabled), the next statement  $p_{i+1}$  becomes enabled. The sequential composition terminates upon termination of its last statement  $p_n$ .

### 2.6.2 Guard operator

Enabling of a guarded statement enables its guard  $b$ . Behavior of a guarded statement  $b \rightarrow p$  depends on the value of the guard  $b$ :

- Statement  $p$  is enabled while the guard is enabled and the value of the guard is true. Execution of the first *action* by  $p$  disables the guard. Thus, after this first action, the value of the guard becomes irrelevant.
- Statement  $p$  is disabled while the value of the guard is false. The guarded statement  $b \rightarrow p$  can, in principle, do any delay while the guard is enabled and its value is false; only at the start point and end point of such a delay, the value of the guard may be true.

When a guarded statement occurs in parallel with another statement, as in  $q \parallel b \rightarrow p$ , the value of the guard can change due to actions of statement  $q$ , which may cause statement  $p$  to change from being disabled to enabled or vice versa. E.g.  $b := \text{false}; (\Delta 1; b := \text{true} \parallel b \rightarrow \text{skip})$ .

When in  $q \parallel b \rightarrow p$ , the guard  $b$  contains continuous or algebraic variables, and  $q$  contains one or more enabled delay predicates, the value of the guard may change during a delay, causing statement  $p$  to change from being disabled to enabled or vice versa. E.g.  $\dot{x} = 1 \parallel x \geq 1 \rightarrow x := 0$ .

### 2.6.3 Alternative composition

Enabling  $p_1 \square \dots \square p_n$  enables the statements  $p_1, \dots, p_n$ . Execution of an action by any one of the statements  $p_1 \dots p_n$  disables the other statements. In this way, execution of the first action makes a choice. When one of the statements  $p_1, \dots, p_n$  terminates, the alternative composition  $p_1 \square \dots \square p_n$  also terminates.

### 2.6.4 Parallelism

Enabling  $p_1 \parallel \dots \parallel p_n$  enables the statements  $p_1, \dots, p_n$ . When a statement  $p_i$ ,  $1 \leq i \leq n$ , executes an action, the other statements remain enabled. The parallel composition  $p_1 \parallel \dots \parallel p_n$  terminates when the statements  $p_1, \dots, p_n$  have all terminated.

Informally, we often refer to the statements  $p_1, \dots, p_n$  occurring in  $p_1 \parallel \dots \parallel p_n$  as parallel processes. Parallel processes interact by means of shared variables or by means of synchronous point-to-point communication or synchronization via a channel. Communication in  $\chi$  is the sending of values of one or more expressions by one parallel process via a channel to another parallel process, where the received values are stored in variables. In case no values are sent and received, we refer to synchronization instead of communication.

### 2.6.5 Loop and while statement

Loop statement  $*p$  represents the infinite repetition of statement  $p$ . When  $*p$  is enabled,  $p$  is enabled. Termination of  $p$  results in re-enabling of  $p$ .

The while statement  $b \xrightarrow{*} p$  can be interpreted as “while  $b$  do  $p$ ”. Enabling of  $b \xrightarrow{*} p$  when  $b$  is true enables  $p$  (by means of an action), and enabling of  $b \xrightarrow{*} p$  when  $b$  is false, leads to termination of the while statement (by means of an action).

### 2.6.6 Variable and channel scope operator

A variable and channel scope operator may introduce new variables and new channels. Enabling of a variable and channel scope statement  $\llbracket D :: p \rrbracket$ , where the local declaration part  $D$  introduces new variables and/or channels (see Sections 2.1 and 2.2), performs the variable initializations specified in  $D$  and enables statement  $p$ . Termination of  $p$  terminates the scope statement  $\llbracket D :: p \rrbracket$ . Any occurrence of a variable or channel in  $p$  that is declared in  $D$  refers to that local variable or

channel and not to any more global declaration of the variable or channel with the same name, if such a more global declaration should exist.

### 3 Algebraic reasoning and verification

#### 3.1 Introduction

The  $\chi$  process algebra has strong support for modular composition by allowing unrestricted combination of operators such as sequential and parallel composition, by providing statements for scoping, by providing process definition and instantiation, and by providing different interaction mechanisms, namely synchronous communication and shared variables.

The fact that the  $\chi$  language is a process algebra with a wide range of statements potentially complicates the development of tools for  $\chi$ , since the implementations have to deal with all possible combinations of the  $\chi$  atomic statements and the operators that are defined on them. This is where the process algebraic approach of equational reasoning, that allows rewriting models to a simpler form, is essential.

To illustrate the required implementation efforts, consider the following implementations that are developed: a Python implementation for rapid prototyping; a C implementation for fast model execution; and an implementation based on the MATLAB Simulink S-functions (The MathWorks, Inc 2005), where a  $\chi$  model is translated to an S-function block. Furthermore, there is an implementation for real-time control (Hofkamp 2001). In (Bortnik, Trčka, Wijs, Luttik, van de Mortel-Fronczak, Baeten, Fokkink, and Rooda 2005) it has been shown that different model checkers each have their own strengths and weaknesses. Therefore, for verification, translations to several tools are defined. In particular, for hybrid models a translation to the hybrid I/O automaton based PHAver (Frehse 2005) model checker is defined. For timed models the following translations are defined: (1) a translation to the action-based process algebra  $\mu$ CRL (Groote 1997), used as input language for the verification tool CADP (Fernandez, Garavel, Kerbrat, Mounier, Mateescu, and Sighireanu 1996); (2) a translation to PROMELA, a state-based, imperative language, used as input language for the verification tool SPIN (Holzmann 2003); and (3) a translation to the timed automaton based input language of the UPPAAL (Larsen, Pettersson, and Yi 1997) verification tool. In future, for verification of hybrid models, additional translations may be considered to tools such as HYTECH (Alur, Henzinger, and Ho 1996), or one of the many other hybrid model checkers.

Instead of defining the implementations mentioned above on the full  $\chi$  language as defined in Section 2.3, the process algebraic approach of equational reasoning makes it possible to transform  $\chi$  models in a series of steps to a (much simpler) normal form, and to define the implementations on the normal form. The original  $\chi$  model and its normal form are bisimilar, which ensures that relevant model properties are preserved. The normal form has strong syntactical restrictions, no parallel composition operator, and is quite similar to a hybrid automaton. Currently, correctness proofs are developed, and in the near future, implementations will be redesigned based on the normal form.

The steps to the normal form are as follows. First of all, the process instantiations are eliminated, by replacing them by their defining bodies, and replacing the formal parameters by actual arguments. Next, parallel composition is eliminated by using laws of process algebra, in particular a so-called *expansion law* (not given here). An example of a process algebra law in  $\chi$  specifying that the guard distributes over alternative composition is  $b \rightarrow (p \parallel q) = b \rightarrow p \parallel b \rightarrow q$ . Finally, the normal form may be simplified further, taking advantage of the fact that it no longer contains parallel composition. Note that it is possible to construct models for which the normal form can-

not be (easily) generated. These exceptions are not discussed in this chapter, since they do not restrict translation to the normal form for practical purposes. For a definition of the normal form see Section 3.5.

### 3.2 Bottle filling line example

Figure 4 shows a bottle filling line, based on (Baeten and Middelburg 2002), consisting of a storage tank that is continuously filled with a flow  $Q_{in}$ , a conveyor belt that supplies empty bottles, and a valve that is opened when an empty bottle is below the filling nozzle, and is closed when the bottle is full. When a bottle has been filled, the conveyor starts moving to put the next bottle under the filling nozzle, which takes one unit of time. When the storage tank is not empty, the bottle filling flow  $Q$  equals  $Q_{set}$ . When the storage tank is empty, the bottle filling flow equals the flow  $Q_{in}$ . The system should operate in such a way that overflow of the tank does not occur. We assume  $Q_{in} < Q_{set}$ .

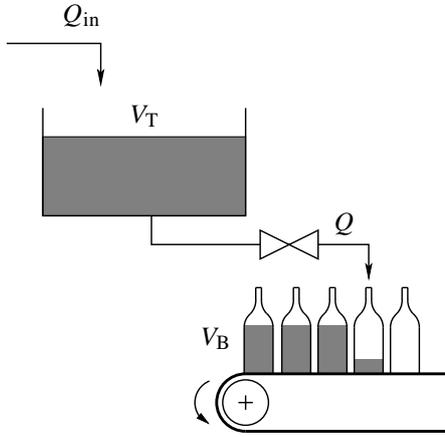


Figure 4: Filling Line.

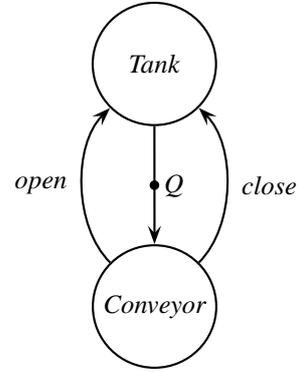


Figure 5: Iconic model of the filling line.

Figure 5 shows an iconic representation of the model of the filling line. It consists of the processes *Tank* and *Conveyor* that interact by means of the channels *open* and *close*, and shared variable  $Q$ . The model is defined below. It has two parameters: the initial volume  $V_{T0}$  of the storage tank, and the value  $Q_{in}$  of the flow that is used to fill the storage tank. The constants  $Q_{set}$ ,  $V_{Tmax}$ , and  $V_{Bmax}$  define the maximum value of the bottle filling flow  $Q$ , the maximum volume of the storage tank, and the filling volume of the bottles, respectively. The model *FillingLine* consists of the algebraic variable  $Q$ , the channels *open* and *close*, and the parallel composition of the process instantiations for the tank and the conveyor.

```

const  $Q_{set}$  : real = 3
      ,  $V_{Tmax}$  : real = 20
      ,  $V_{Bmax}$  : real = 10

model FillingLine(val  $V_{T0}$ ,  $Q_{in}$  : real) =
  || alg  $Q$  : real, chan open, close : void
  :: Tank( $Q$ , open, close,  $V_{T0}$ ,  $Q_{in}$ ) || Conveyor( $Q$ , open, close)
  ||

```

The tank process has a local continuous variable  $V_T$  that is initialized to  $V_{T0}$ . Its process body is a recursion scope consisting of three modes: closed, opened, and openedempty that correspond

to the valve being closed, the valve being open, and the valve being open while the storage tank is empty. The syntax and semantics of recursion scopes is defined in Section 3.3. In the mode opened, the storage tank is usually not empty. When the storage tank is empty in mode opened, the delayable skip statement [skip] may be executed causing the next mode to be openedempty. Due to the consistent equation semantics, the skip statement can be executed only if the delay predicate in the next mode openedempty holds. This means, among others, that  $V_T = 0$  must hold. Therefore, the transition to mode openedempty can be taken only when the storage tank is empty. Note that the comma in delay predicates denotes conjunction. E.g.  $\dot{V}_T = Q_{in}, Q = 0$  means  $\dot{V}_T = Q_{in} \wedge Q = 0$ .

```

proc Tank(alg Q : real, chan open?, close? : void, val VT0, Qin : real) =
  [[ cont VT : real = VT0
  :: [[ mode closed =
      (  $\dot{V}_T = Q_{in}, Q = 0, V_T \leq V_{Tmax}$  || open?; opened )
    , mode opened =
      (  $\dot{V}_T = Q_{in} - Q, Q = Q_{set}, 0 \leq V_T \leq V_{Tmax}$ 
        || [skip]; openedempty
        || close?; closed
      )
    , mode openedempty =
      (  $V_T = 0, Q = Q_{in}$  || close?; closed )
  :: closed
  ]]
]]

```

Process *Conveyor* supplies an empty bottle in 1 unit of time ( $V_B := 0; \Delta 1$ ). Then it synchronizes with the storage tank process by means of the send statement *open!*, and it proceeds in mode filling. When the bottle is filled in mode filling ( $V_B \geq V_{Bmax}$ ), the process synchronizes with the storage tank to close the valve and returns to mode moving. The initial mode is moving.

```

proc Conveyor(alg Q : real, chan open!, close! : void) =
  [[ cont VB : real = 0
  :: [[ mode moving = (  $V_B := 0; \Delta 1; open!$ ; filling )
    , mode filling = (  $V_B \geq V_{Bmax} \rightarrow close!$ ; moving )
  :: moving
  ]]
  ||  $\dot{V}_B = Q$ 
  ]]

```

Figure 6 shows the results of the first 12 time units of a simulation run of the model *FillingLine*(5, 1.5), that is with model parameters  $V_{T0} = 5$  and  $Q_{in} = 1.5$ . The graph shows that the first bottle is filled from time point 1 until time point  $1 + 10/3 \approx 4.33$ . Filling of the second bottle starts 1 time unit later, and somewhat after 7 time units, the storage tank becomes empty, so that filling continues at the reduced flow rate.

### 3.3 Syntax and semantics of the recursion scope operator

The syntax of the recursion scope operator statement  $p_R$ , that was introduced in Section 2.3, and first used in Section 3.2 is defined as:

$$p_R ::= \ll \text{mode } X = p^+ \{, \text{mode } X = p^+\}^* :: X \ll,$$

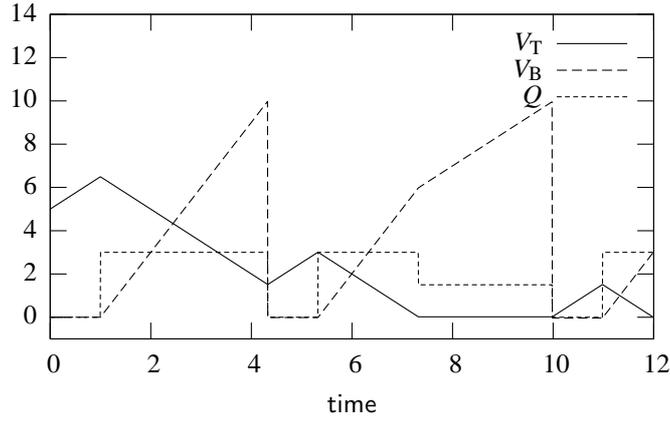


Figure 6: Simulation results of model *FillingLine*.

where  $X$  denotes a recursion variable, and statements  $p^+$  consist of statements  $p$  (see Section 2.3) to which recursion variables  $X$  are added:

$$p^+ ::= p \mid p; X \mid p^+ \parallel p^+ \mid b \rightarrow p^+ \mid p; p^+$$

The syntax enforces any recursion variable  $X$  to occur only at the end of a sequential composition. An additional restriction is that each recursion scope operator must be ‘complete’. This means that in

$$\llbracket \text{mode } X_1 = p_1^+, \dots, \text{mode } X_n = p_n^+ :: X_k \rrbracket,$$

all occurrences of free recursion variables in  $p_i^+$  ( $1 \leq i \leq n$ ) must be defined in the recursion scope operator itself. These restrictions enforce structured use of recursion: only one recursion variable  $X_i$  with corresponding statement  $p_i^+$  can be executed at the same time, and termination of any of the statements  $p_i$  terminates the scope operator itself. This structured use of recursion simplifies analysis of  $\chi$  models, it simplifies the translation to the normal form as discussed in Section 3, and it simplifies tool support for  $\chi$ .

The meaning of recursion scope operators is as follows. Enabling the recursion scope operator  $\llbracket X_1 = p_1^+, \dots, X_n = p_n^+ :: X_i \rrbracket$ , enables the statement  $X_i$  ( $1 \leq i \leq n$ ). When a recursion variable  $X_j$  ( $1 \leq j \leq n$ ) is enabled (or disabled), its defining statement  $p_j$  is enabled (or disabled) instead. When a defining statement  $p_j$  terminates, the recursion scope operator terminates.

### 3.4 Elimination of process instantiation

Elimination of the process instantiations for the *Tank* and *Conveyor* processes by replacing the process instantiations by their definitions, as defined in (Beek, Man, Reniers, Rooda, and Schiffelers 2006), leads to the following model:

```

model FillingLine(val  $V_{T0}, Q_{in} : \text{real}$ ) =
  [[ alg  $Q : \text{real}$ , chan  $open, close : \text{void}$ 
  :: [[ cont  $V_T : \text{real} = V_{T0}^L$ 
    , var  $V_{T0}^L : \text{real} = V_{T0}, Q_{in}^L : \text{real} = Q_{in}$ 
    :: [[ mode closed =
      (  $\dot{V}_T = Q_{in}^L, Q = 0, V_T \leq V_{Tmax} \parallel open?; opened$  )
  ]]]

```

```

, mode opened =
  (  $\dot{V}_T = Q_{in}^L - Q$ ,  $Q = Q_{set}$ ,  $0 \leq V_T \leq V_{Tmax}$ 
     $\parallel$  [skip]; openedempty
     $\parallel$  close?; closed
  )
, mode openedempty =
  (  $V_T = 0$ ,  $Q = Q_{in}^L$   $\parallel$  close?; closed )
:: closed
 $\parallel$ 
 $\parallel$ 
 $\parallel$   $\parallel$  cont  $V_B$  : real = 0
::  $\parallel$  [ mode moving = (  $V_B := 0$ ;  $\Delta 1$ ; open!; filling )
, mode filling = (  $V_B \geq V_{Bmax} \rightarrow$  close!; moving )
:: moving
 $\parallel$ 
 $\parallel$   $\dot{V}_B = Q$ 
 $\parallel$ 
 $\parallel$ 

```

To avoid naming conflicts between the formal parameters  $V_{T0}$  and  $Q_{in}$  declared in the process definition for process *Tank*, and the actual arguments  $V_{T0}$  and  $Q_{in}$  in the process instantiation  $Tank(Q, open, close, V_{T0}, Q_{in})$ , the newly defined local discrete variables that are used to hold the values of the last two parameters of the process instantiation, are renamed to  $V_{T0}^L$  and  $Q_{in}^L$ .

### 3.5 Syntax of the normal form

A slightly simplified syntax for the normal form in  $\chi$  is given by a model with on the outer level a global variable and channel declaration  $D$  (see Sections 2.1 and 2.2), on the inner level a local variable and channel declaration  $D$ , and one recursion scope operator statement:

$$\chi_{norm} ::= \text{model } id(D_m) = \llbracket D :: \llbracket D :: \llbracket X = p_{norm} \{, X = p_{norm}\}^* :: X \rrbracket \rrbracket \rrbracket,$$

The normalized statements  $p_{norm}$ , used to define the recursion variables  $X$ , may consist of undelayable normalized atomic statements  $p_{na}$  (defined below). Such a normalized atomic statement may be prefixed by a guard  $b$ , and/or it may be made delayable (e.g.  $b \rightarrow p_{na}$  and  $[p_{na}]$ ). Sequential composition is allowed only in the form of such (guarded, and/or delayable) atomic statements followed by a recursion variable. Finally, all of these statements may be part of alternative composition:

$$\begin{array}{ll}
p_{norm} ::= p_{nga} & \text{(guarded) atomic action} \\
| u & \text{delay predicate} \\
| p_{nga}; X & \text{atomic action followed by recursion variable} \\
| p_{norm} \parallel p_{norm} & \text{alternative composition,}
\end{array}$$

where the normalized guarded atomic action statements  $p_{nga}$  are defined by:

$$\begin{array}{ll}
p_{nga} ::= p_{na} & \text{non-delayable atomic action statement} \\
| b \rightarrow p_{na} & \text{guarded non-delayable atomic action statement} \\
| [p_{na}] & \text{delayable atomic action statement} \\
| b \rightarrow [p_{na}] & \text{guarded delayable atomic action statement,}
\end{array}$$

and the normalized atomic action statements  $p_{na}$ , that are all non-delayable, are defined by:

$p_{na} ::=$	<code>skip</code>	skip statement
	<code>x := e</code>	multi-assignment
	<code>h !?</code>	synchronization via channel $h$
	<code>h !? x := e</code>	communication via channel $h$

The synchronization statement  $h !?$  and communication statement  $h !? x := e$  are required because of the fact that there is no parallel composition in the normalized form. The parallel composition  $h ! \parallel h ?$  is normalized to  $h !?$ , and  $h ! e \parallel h ? x$  is normalized to  $h !? x := e$ . The statement  $h !?$  is comparable to the skip statement, and the statement  $h !? x := e$  is comparable to the multi-assignment statement  $x := e$ . The effect on the values of the variables is the same. There is only a small difference with respect to the occurrence of channel  $h$ , possibly accompanied by the value of  $e$ , in the transition system.

As an example, that clarifies how the delay statement is eliminated in the translation to the normal form, consider the statement  $x := 2; \Delta 1$  which is first rewritten as  $x := 2; \llbracket \text{cont } t : \text{real} = 1 :: \dot{t} = -1 \parallel t \leq 0 \rightarrow \text{skip} \rrbracket$  and then normalized to

```

[[ cont t : real
:: [[ X0 = ( x, t := 2, 1; X1 )
   , X1 = (  $\dot{t} = -1 \parallel t \leq 0 \rightarrow \text{skip}$  )
   :: X0
   ]]
]]

```

The normal form makes it easy to analyze system behavior and it simplifies tool implementations in the following way. When a model is defined as

```

model M(val x : t) =
[[ D0
:: [[ D1
   :: [[ X1 =  $p_{norm_1}, \dots, X_n = p_{norm_n} :: X_i$  ]]
   ]]
]],

```

$M(c)$  defines a particular model instantiation. At each point of execution of this model instantiation, exactly one recursion variable  $X_i$  is enabled, so that the set of all possible next steps is determined by the term  $p_{norm_i}$  only. In addition, the term  $p_{norm_i}$  defines for each action the recursion variable (if any) that is enabled after execution of the action. Process definition, process instantiation, parallel composition, send and receive statements, the loop statement, while do statement, and delay statement are no longer present. Also scoping has been eliminated, apart from one top level variable and channel scope operator, and one top level recursion scope operator.

### 3.6 Elimination of parallel composition

Elimination of parallel composition and translation to the normal form as discussed in Section 3.5 leads to the model:

```

model FillingLine(val VT0, Qin : real) =
[[ alg Q : real, chan open, close : void
:: [[ cont VT : real = VT0L, VB : real = 0

```

```

, cont  $t$  : real, var  $V_{T0}^L$  : real =  $V_{T0}$ ,  $Q_{in}^L$  : real =  $Q_{in}$ 
::  $\llbracket$  moving_closed =
  (  $\dot{V}_T = Q_{in}^L$ ,  $Q = 0$ ,  $V_T \leq V_{Tmax}$ ,  $\dot{V}_B = Q$ 
     $\llbracket$   $V_B, t := 0, 1$ ; moving0_closed
  )
, moving0_closed =
  (  $\dot{V}_T = Q_{in}^L$ ,  $Q = 0$ ,  $V_T \leq V_{Tmax}$ ,  $\dot{V}_B = Q$ ,  $i = -1$ 
     $\llbracket$   $t \leq 0 \rightarrow$  skip; moving1_closed
  )
, moving1_closed =
  (  $\dot{V}_T = Q_{in}^L$ ,  $Q = 0$ ,  $V_T \leq V_{Tmax}$ ,  $\dot{V}_B = Q$ 
     $\llbracket$  open !?; filling_opened
  )
, filling_opened =
  (  $\dot{V}_T = Q_{in}^L - Q$ ,  $Q = Q_{set}$ ,  $0 \leq V_T \leq V_{Tmax}$ ,  $\dot{V}_B = Q$ 
     $\llbracket$  [skip]; filling_openedempty
     $\llbracket$   $V_B \geq V_{Bmax} \rightarrow$  close !?; moving_closed
  )
, filling_openedempty =
  (  $V_T = 0$ ,  $Q = Q_{in}^L$ ,  $\dot{V}_B = Q$ 
     $\llbracket$   $V_B \geq V_{Bmax} \rightarrow$  close !?; moving_closed
  )
:: moving_closed
 $\llbracket$ 
 $\llbracket$ 
 $\llbracket$ 

```

### 3.7 Substitution of constants and additional elimination

The model below is the result of substitution of the globally defined constants by their values. Furthermore, the discrete variables  $Q_{in}^L$  and  $V_{T0}^L$ , that were introduced by elimination of the process instantiations, are eliminated. Also, the presence of the undelayable statements  $V_B, t := 0, 1$  and *open !?* in modes moving<sub>0</sub>\_closed and moving<sub>1</sub>\_closed, respectively, allows elimination of the differential equations in these modes.

Most hybrid automaton based model checkers, such as PHAver (Frehse 2005) and HYTECH (Henzinger, Ho, and Wong-Toi 1995), do not (yet) have urgent transitions that can be combined with guards. Therefore, the urgency in the guarded statements is removed by making the statements that are guarded delayable, and adding the closed negation of the guard as an additional delay predicate (invariant). E.g.  $t \leq 0 \rightarrow$  skip is rewritten as  $t \geq 0 \llbracket t \leq 0 \rightarrow$  [skip].

```

model FillingLine(val  $V_{T0}, Q_{in}$  : real) =
 $\llbracket$  alg  $Q$  : real, chan open, close : void
::  $\llbracket$  cont  $V_T$  : real =  $V_{T0}$ ,  $V_B$  : real = 0,  $t$  : real
::  $\llbracket$  moving_closed =
  (  $V_T \leq 20$ ,  $Q = 0$ 
     $\llbracket$   $V_B, t := 0, 1$ ; moving0_closed
  )
, moving0_closed =
  (  $\dot{V}_T = Q_{in}$ ,  $Q = 0$ ,  $V_T \leq 20$ ,  $\dot{V}_B = 0$ ,  $i = -1$ ,  $t \geq 0$ 

```

```

    ||  $t \leq 0 \rightarrow$  [skip]; moving1_closed
  )
, moving1_closed =
  (  $V_T \leq 20, Q = 0$ 
    || open !?; filling_opened
  )
, filling_opened =
  (  $\dot{V}_T = Q_{in} - 3, Q = 3, 0 \leq V_T \leq 20, \dot{V}_B = 3, V_B \leq 10$ 
    || [skip]; filling_openedempty
    ||  $V_B \geq 10 \rightarrow$  [close !?]; moving_closed
  )
, filling_openedempty =
  (  $V_T = 0, Q = Q_{in}, \dot{V}_B = Q, V_B \leq 10$ 
    ||  $V_B \geq 10 \rightarrow$  [close !?]; moving_closed
  )
:: moving_closed
||
||
||

```

Figure 7 shows a graphical representation of the model. By means of straightforward mathematical analysis of the model, it can be shown that overflow never occurs if  $Q_{in} \leq 30/13$ .

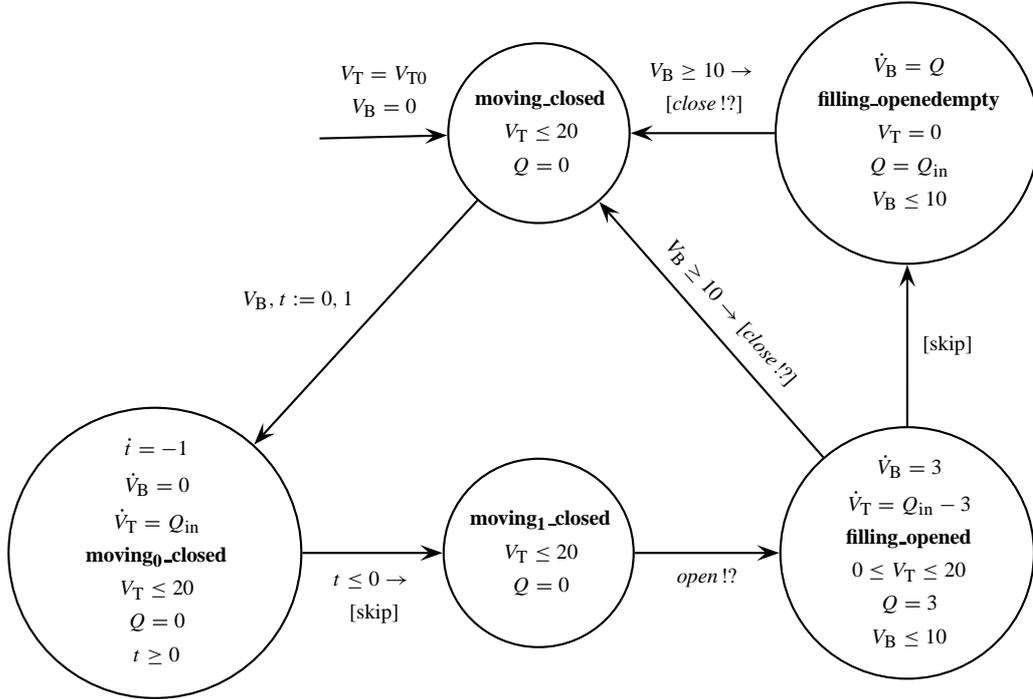


Figure 7: Graphical representation of the normalized  $\chi$  model.

### 3.8 Tool based verification

As a final step, for the purpose of tool-based verification, the model is translated to the input language of the hybrid IO automaton based tool PHAVer (Frehse 2005). Since most hybrid automata, including PHAVer, do not know the concept of an algebraic variable, first the algebraic variables are eliminated from the  $\chi$  model. Because of the consistent equation semantics of  $\chi$ , each occurrence of an algebraic variable in the model can simply be replaced by the right hand side of its defining equation. The urgency due to unguarded undelayable statements is in principle translated by defining the corresponding flow clause as false. The resulting PHAVer model follows below. Note that an additional variable  $x$  is introduced and the derivatives of  $V_b$  and  $V_t$  need to be defined in all locations, because of the current inability of PHAVer to define false as flow clause.

```

automaton filling_line
state_var: Vt,Vb,t,x;
parameter: Vt0,Qin;
synclabs : open,close,tau;
loc moving_closed:
  while Vt <= 20 & x==0 wait {x'==1 & Vb'==0 & Vt'==0};
  when true sync tau do {Vt'==Vt & Vb'==0 & t'==1 & x'==0}
    goto moving0_closed;
loc moving0_closed:
  while Vt <= 20 & t >= 0 wait {Vb'==0 & t'==-1 & Vt==30/13};
  when t <= 0 sync tau do {Vt'==Vt & Vb'==Vb & t'==t & x'==0}
    goto moving1_closed;
loc moving1_closed:
  while Vt <= 20 & x==0 wait {x'==1 & Vb'==0 & Vt'==0};
  when true sync open do {Vt'==Vt & Vb'==Vb & t'==t}
    goto filling_opened;
loc filling_opened:
  while Vt >= 0 & Vt <= 20 & Vb <= 10 wait {Vb'==3 & Vt'==30/13-3};
  when Vt==0 sync tau do {Vt'==Vt & Vb'==Vb & t'==t}
    goto filling_openedempty;
  when Vb >= 10 sync close do {Vt'==Vt & Vb'==Vb & t'==t & x'==0}
    goto moving_closed;
loc filling_openedempty:
  while Vt == 0 & Vb <= 10 wait {Vb'==30/13};
  when Vb >= 10 sync close do {Vt'==Vt & Vb'==Vb & t'==t & x'==0}
    goto moving_closed;
initially moving_closed & Vt == Vt0 & Vb==0 & x==0;
end

```

The following properties were derived: if  $Q_{in} = 30/13$  and  $0 \leq V_{T0} \leq V_{Tmax} - 30/13$ , overflow does not occur, and the storage tank does not become empty when filling a bottle. The volume of the storage tank then remains in the region  $V_{T0} \leq V_T \leq V_{T0} + 30/13$ . If  $Q_{in} > 30/13$ , eventually overflow occurs. If  $Q_{in} < 30/13$ , eventually the container becomes empty every time a bottle is filled. In this small example, these properties can also be derived by means of straightforward mathematical analysis of the  $\chi$  models of Section 3.6 or 3.7.

## 4 Conclusions

Process algebra originated in the domain of theoretical computer science, where it was designed for the purpose of reasoning about the behavior of concurrent discrete-event systems. Recently, process algebra theory has been extended to include also continuous-time systems, and combined discrete-event / continuous-time, or hybrid systems. The  $\chi$  process algebra, that has been used

as an example in this chapter, illustrates that process algebra is not only suited to verification, but also very well suited to high level modeling and simulation of complex dynamical systems. The compositional semantics of a process algebra facilitates modular composition of processes and statements using not only parallel composition, but also sequential composition, and in fact any kind of combination of statements by means of the process algebra operators. The equational reasoning, that is characteristic of process algebra, allows rewriting of complex specifications to a straightforward normal form, where parallel composition and many other operators and statements have been eliminated. For the  $\chi$  process algebra, the normal form is very similar to a hybrid automaton, and thus simplifies the use and development of tools for simulation and verification.

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